

Abstract of the Disclosur

5 An output circuit having undershoot/overshoot reduction, and improved propagation delay. A damping control circuit branch is provided, including a resistor and a diode connected in parallel between a first node and a second node, the second node being coupled to an output node. An output transistor, having a gate, is coupled by its source and drain between a power supply and the second node. A predriver circuit is adapted to receive an input signal and provide a voltage at the gate.